

SPIM Command-Line Options

All versions of SPIM (`spim`, `xspim`, and `PCSpim`) have command-line options that control how SPIM starts running. The general format is

```
spim arguments file.s program_arguments
```

where *spim* is the name of a particular version of SPIM (`spim`, `PCSpim`, or `xspim`), *arguments* are the command-line options described below, *file.s* is the name of a file containing a MIPS program, and *program_arguments* are the initial arguments passed to the MIPS program. For example, to just start `xspim` without any arguments or an initial program, type

```
xspim
```

Or, to start `PCSpim` with delayed branches on file `test.s`, type

```
pcspim -delayed_branches test.s
```

The programs accept the following command-line options:

<code>-asm, -a</code>	Simulate the virtual MIPS machine provided by the assembler. This is the default.
<code>-bare, -b</code>	Simulate a bare MIPS machine without pseudoinstructions or the additional addressing modes provided by the assembler. Implies the flags <code>-quiet</code> , <code>-delayed_branches</code> , and <code>-delayed_loads</code> .
<code>-delayed_branches, -db</code>	Execute the instruction following a branch or jump in its delay slot. The offset in a branch must be adjusted to reflect that the control transfer occurs when the PC points to the instruction in the delay slot. The default does not simulate the delay slot, so the offset is relative to the branch instruction.
<code>-delayed_loads, dl</code>	A value loaded from memory is not available to the instruction executed immediately after the load instruction. The default is that loaded values are available immediately.
<code>-exception, -e</code>	Load the standard exception handler and start-up code. This is the default.
<code>-noexception, -ne</code>	Do not load the standard exception handler or start-up code. The program must provide code to

	handle exceptions. When an exception occurs, SPIM jumps to location 80000180 _{hex} . The start-up code invokes the routine <code>main</code> with arguments <code>argc</code> and <code>argv</code> . Without the start-up routine, SPIM starts execution at the instruction labeled <code>__start</code> .
<code>-exception_file, ef</code>	Load the the exception handler and start-up code from this file instead of the default file.
<code>-mapped_io, mio</code>	Enable the memory-mapped I/O facility (see Section A.8). Programs that use SPIM <code>syscalls</code> (see section on "System Calls," page A-43) to read from the terminal <i>cannot</i> also use memory-mapped I/O.
<code>-nomapped_io, nmio</code>	Disable the memory-mapped I/O facility (see Section A.8). This is the default.
<code>-pseudo, -p</code>	Allow the input assembly code to contain pseudo-instructions. This is the default.
<code>-nopseudo, -np</code>	Do not allow pseudoinstructions in the input assembly code.
<code>-quiet, -q</code>	Do not print a message when exceptions occur.
<code>-noquiet, -nq</code>	Print a message when an exception occurs. This is the default.
<code>-file <name>, -f</code>	Load and execute the assembly code in the file whose name is given.
<code>-s <seg> size, -st, -sd, -ss, -skt, -skd</code>	Sets the initial size of memory segment <i>seg</i> to be <i>size</i> bytes. The memory segments are named <code>text</code> , <code>data</code> , <code>stack</code> , <code>ktext</code> , and <code>kdata</code> . The <code>text</code> segment contains instructions from a program. The <code>data</code> segment holds the program's data. The <code>stack</code> segment holds its runtime stack. In addition to running a program, SPIM also executes system code that handles interrupts and exceptions. This code resides in a separate part of the address space called the <i>kernel</i> . The <code>ktext</code> segment holds this code's instructions, and <code>kdata</code> holds its data. There is no <code>kstack</code> segment since the system code

	uses the same stack as the program. For example, the pair of arguments <code>-sdata 2000000</code> starts the user data segment at 2,000,000 bytes.
<code>-l <seg> size, -ld, -ls, -lkd</code>	Sets the limit on how large memory segment <i>seg</i> can grow to be <i>size</i> bytes. The memory segments that can grow are <code>data</code> , <code>stack</code> , and <code>kdata</code> .